

## 1x1, 1x2, 2x2, 1x4, for special fibers (SF Series)

### Overview

The SF-series are opto-mechanical switches for the most demanding applications in fiber optic sensor systems and instrumentation. The switch is available in 1x1, 1x2, 2x2 and 1x4 variants and offers solid state reliability, accurate precision and fast response time. The switch mechanism is available in either latching or non latching variants and has a very fast response time below 1 ms and below 1.5 dB insertion loss. The single mode switch is available for a number of specialty fibers covering design wavelengths such as 488 nm, 515 nm, 633 nm, 680 nm, 780 nm, 830 nm, 980 nm and 1064 nm.

The miniature package withstands rugged environments and is well suited for direct mounting on printed circuit boards. The switch is qualified according to Telcordia GR 1221.

### Features

- reliable
- specialty fibers
- 1.5 dB insertion loss
- 1 ms response time
- low PDL
- 60 dB crosstalk
- miniature size
- 2x2, 2x1, 1x1 variants

### Applications

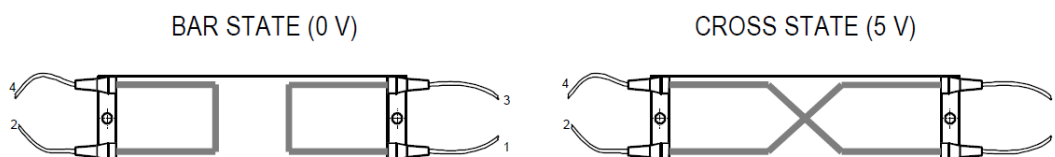
- Instrumentation
- Source selection

### Description

The Mems-Switches are composed of an optical subsystem and an electrical driver interface. The optical switching function is realized by a silicon MEMS chip, on which a mirror can be moved in and out of the optical path by electrostatic actuation. In the latching SF-L variants a bistable suspension mechanism keeps the last selected state in power off. In the non-latching SF-N variants the switch returns into the bar state when electrical power is removed.

To operate the switch 5V and 0V are applied on the supply pins, which are used by the internal DC-DC converter to supply a high voltage for the actuator control. CMOS or TTL logic levels on the control pins switch the high voltage on the electrostatic actuator. To set the switch state in the latching variant, pin 2 respectively pin 3 are set to logic high (5V) for 10 ms and the corresponding switch state is selected. At rest pins 3 and 4 should be pulled to 0 V and must not be floating.

In the non-latching variant only pin 2 is used to set the state of the switch. To set the cross state pin 3 must be at logic high. When pin 3 goes to logic low, or at power off, the switch returns into the bar state. Technology by Sercalo.



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### Technical Specifications

|                             | Unit   | Min     | Typ               | Max   |
|-----------------------------|--------|---------|-------------------|-------|
| <b>Optical Switch</b>       |        |         |                   |       |
| Wavelength Range            | nm     | Cut off | ~ Cut off +200 nm |       |
| Insertion Loss <sup>1</sup> | dB     |         | 0,7               | 1,5   |
| Crosstalk                   | dB     |         | 75                | 60    |
| Backreflection              | dB     |         | 55                | 50    |
| Polarisation Dependent Loss | dB     |         | 0,02              | 0,05  |
| Repeatability <sup>2</sup>  | dB     |         |                   | 0,001 |
| Switching Time              | ms     |         | 0,5               | 1     |
| Durability                  | cycles |         | 3 billion         |       |
| <b>Package</b>              |        |         |                   |       |
| Voltage                     | V      | 4       | 5                 | 5,25  |
| Power Consumption           | mW     | 0       | 5                 | 10    |
| Operation Temperature       | °C     | 0       |                   | 70    |
| Storage Temperature         | °C     | -40     |                   | 85    |
| Size (L x W x H)            | mm     |         | 40 x 16,5 x 9,5   |       |

<sup>1</sup> value excluding connectors. Add 0.25 dB to account for temperature and wavelength dependent loss.

<sup>2</sup> value for constant temperature and polarisation

### PIN Location

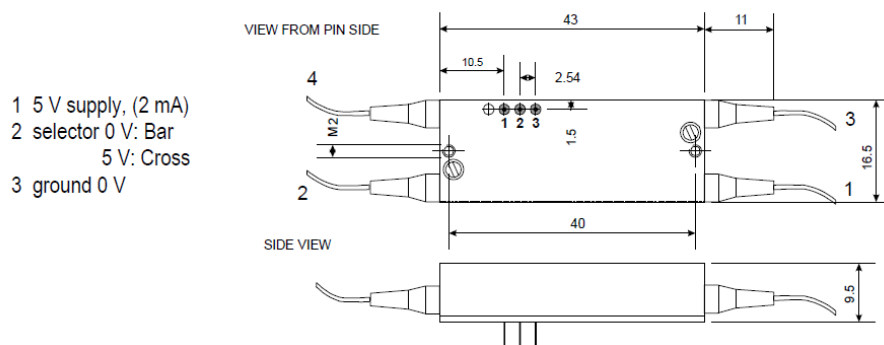


Figure 1: Pin layout SF-N2x2: non-latching

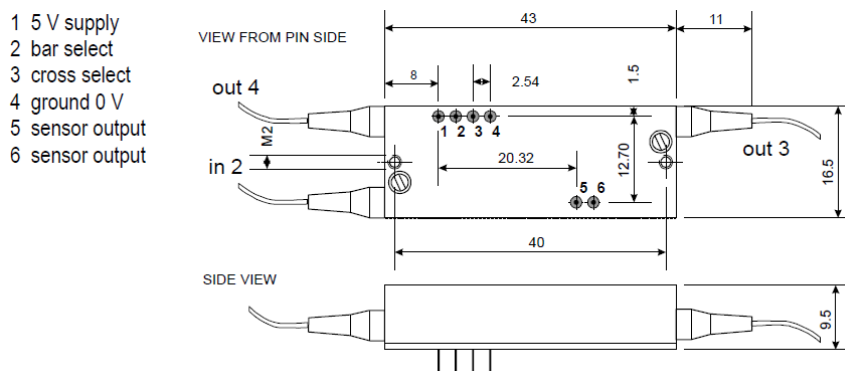


Figure 2: Pin layout MO1x2: latching with position monitor. Without monitor, pins 5,6 are omitted.

## 1x1, 1x2, 2x2, 1x4, for special fibers (SF Series)

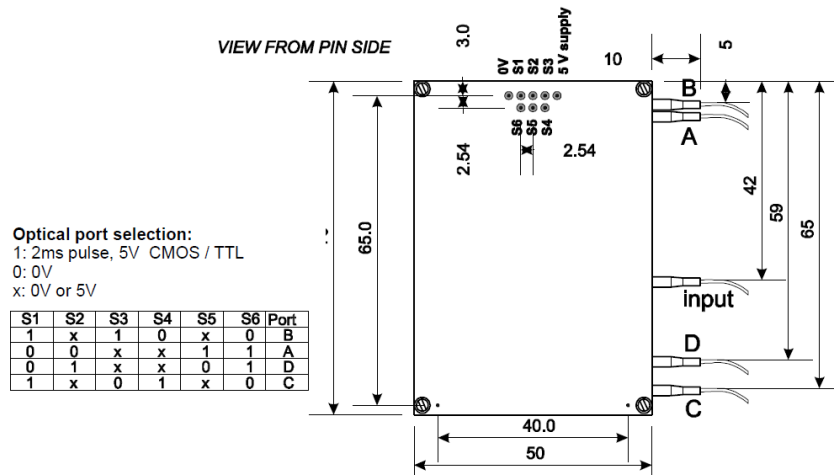
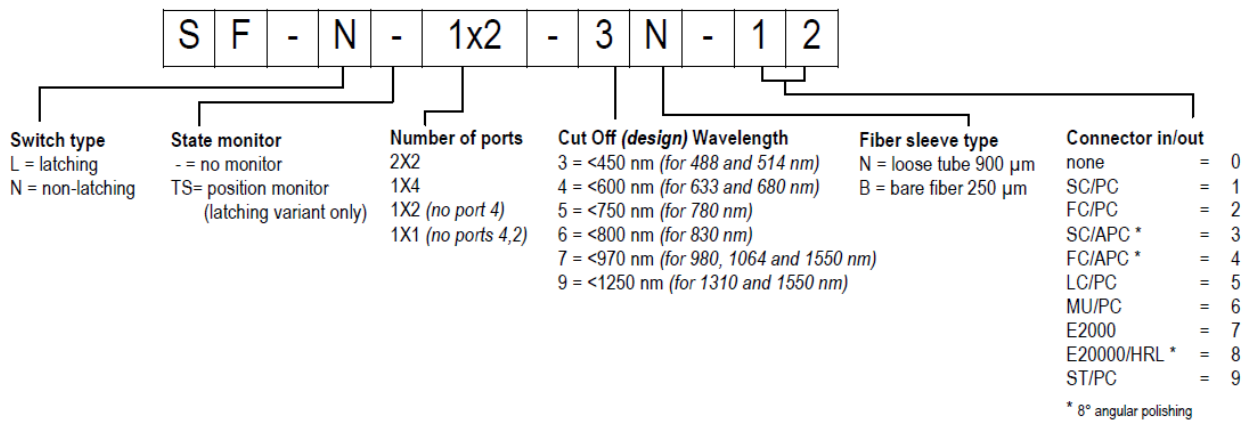


Figure 3: Pin layout SF1x4: latching. In the non-latching variant pins S4, S5, S6 are omitted.

## Ordering Information



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